

**Standard Input/Output Stations**

- Rugged, Fully Potted Stations
- IP 67, IP 68, IP 69K Protection
- Screw Terminal Connections
- Automatic Baud Rate Sensing



**FDNP-S0808G-ST**  
**FDNP-XSG16-ST**



**Electrical**

- Operating Current: <75 mA from DeviceNet (for ...S0808G... add input currents)
- Sensor Current: <700 mA total of all inputs (...S0808G... From DeviceNet, ...XSG16... from aux. Power)
- Output Current: <500 mA per output (from aux. power)

**Power Distribution**

- Inputs: ...S0808G... from DeviceNet power supply, ...XSG16... From Auxiliary power supply
- Outputs: Auxiliary power supply

**Mechanical**

- Operating Temperature: -40 to +70°C (-40 to +158°F)
- Protection: NEMA 1,3,4,12,13 / IEC IP 67, IP 68, IP 69K
- Vibration: 50 g @ 10-500 Hz

**Material**

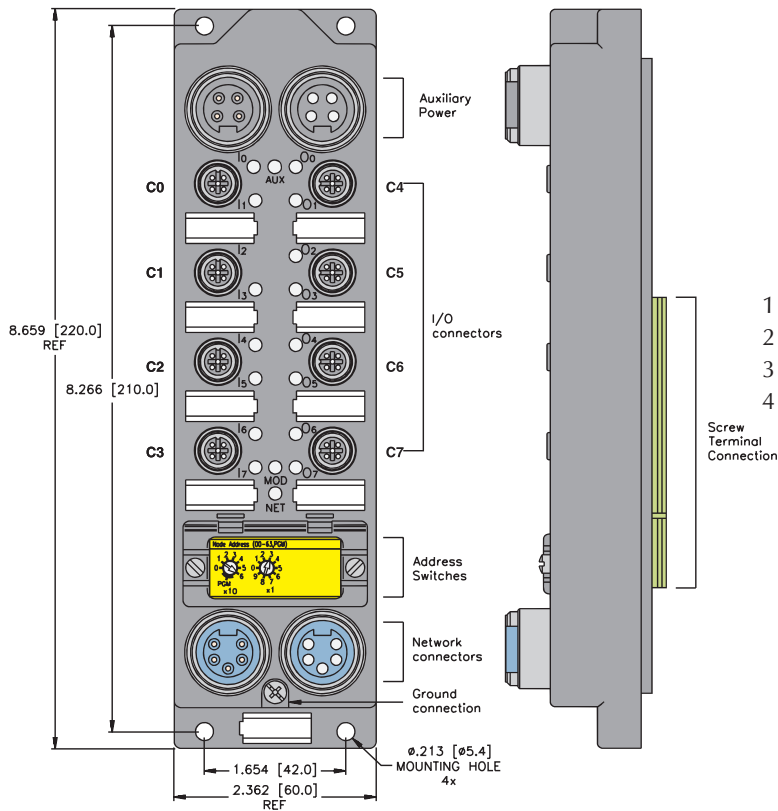
- Connectors: Nickel-plated brass (stainless steel available on request)
- Housing: Nylon 6 (other materials available on request)

**Diagnostics (Logical)**

- Open/short-circuit status mapped to DeviceNet I/O table, one bit indicates a fault for the entire station

**Diagnostics (Physical)**

- One LED indicates an I/O fault for the entire station
- LEDs to indicate status of DeviceNet communication



**Aux. Power Pinout**

Male	Female
<b>4-Pin</b>	<b>4-Pin</b>

- 1 =  $V_{AUX+}$
- 2 = pass thru
- 3 = pass thru
- 4 =  $V_{AUX-}$

**DeviceNet minifast Pinout**

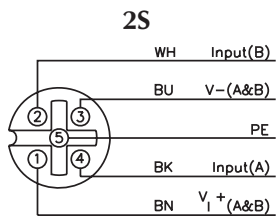
Male	Female
<b>5-Pin</b>	<b>5-Pin</b>

- 1 = Shield
- 2 =  $V+$
- 3 =  $V-$
- 4 = CAN\_H
- 5 = CAN\_L

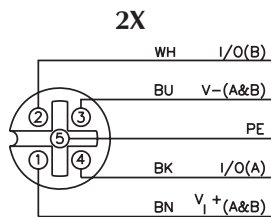


Part Number	Inputs								Outputs					Data		
	Input Count	Connectors	Pinout	Inputs per Connector	Sensor Style	Group Diagnostics	Individual Diagnostics	Wire-Break Detection	Output Count	Connectors	Pinout	Outputs per Connector	Current	Individual Diagnostics	Wire-Break Detection	I/O Map
FDNP-S0808G-ST	8	0-3	2S, ST1	2	PNP	X			8	4-7	2G, ST1	2	0.5 A			1
FDNP-XSG16-ST	16	0-7	2X, ST2	2	PNP	X			16	0-7	2X, ST2	2	0.5 A			2

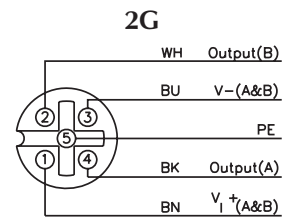
Input/Output Connectors



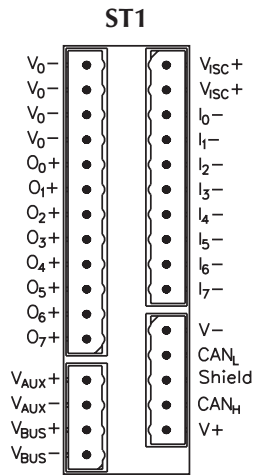
**Mating cordset:**  
RK 4.4T-\*-RS 4.4T  
**Splitter:**  
VBRS 4.4-2RK 4T-\*/\*



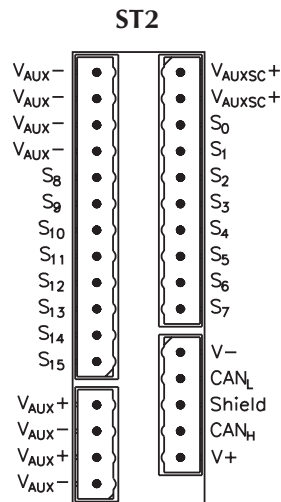
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NOTE:  $V_{isc}$  is from DeviceNet power supply.  
 $V_0$  is from Auxiliary power supply.



I/O Data Map 1

In	Byte	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	0	I-7	I-6	I-5	I-4	I-3	I-2	I-1	I-0
1	IGS	OGS	-	-	-	-	-	-	-
Out	0	0-7	0-6	0-5	0-4	0-3	0-2	0-1	0-0

I/O Data Map 2

In	Byte	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
	0	I-7	I-6	I-5	I-4	I-3	I-2	I-1	I-0
1	I-15	I-14	I-13	I-12	I-11	I-10	I-9	I-8	
2	IGS	OGS	-	-	-	-	-	-	
Out	0	0-7	0-6	0-5	0-4	0-3	0-2	0-1	0-0
1	0-15	0-14	0-13	0-12	0-11	0-10	0-9	0-8	